

FIGURE 1A

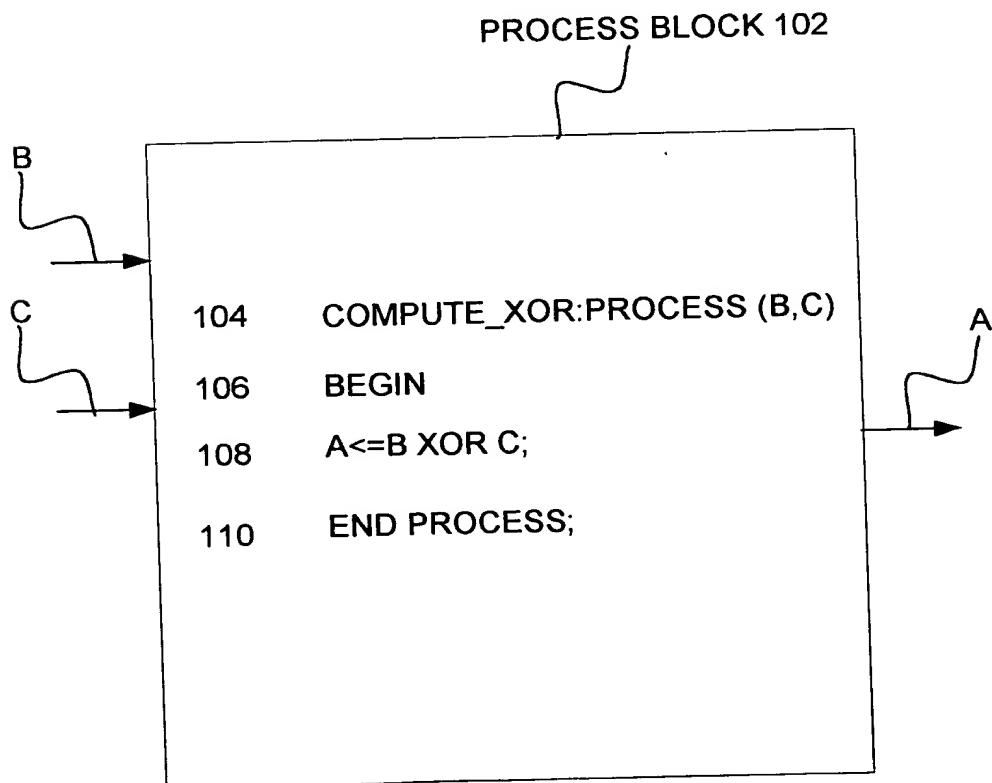


FIGURE 1B

FIGURE 2A

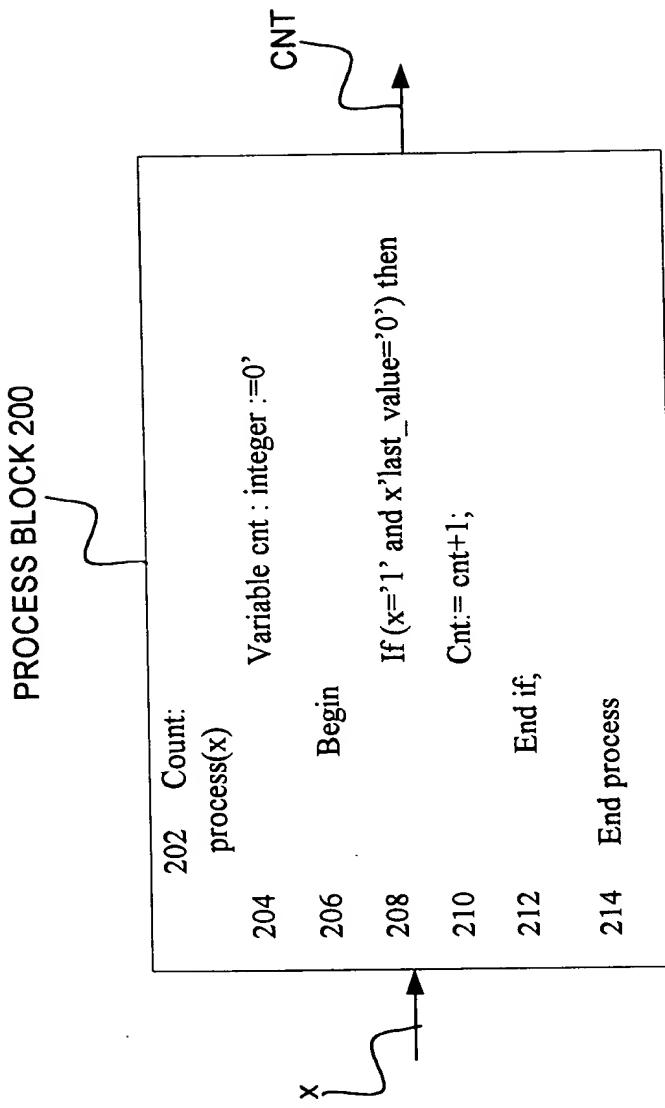
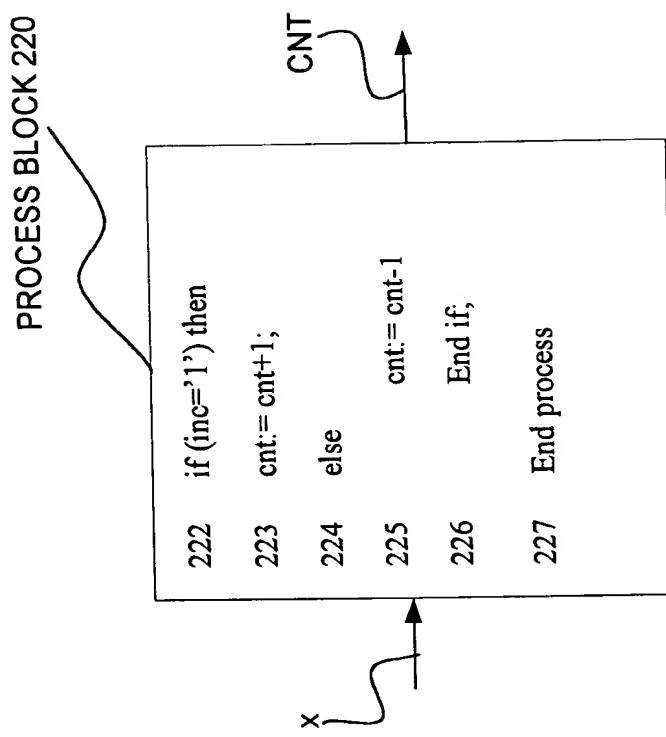


FIGURE 2B



PROCESS BLOCK 230

```
231  Signal x:bit_vector (7 downto 0); process (x)
232
233      variable p : bit;
234          begin
235              p:='0';
236          for I in 7 downto 0 loop
237              p:=p xor x(I);
238          end loop;
239      end process;
```

FIGURE 2C

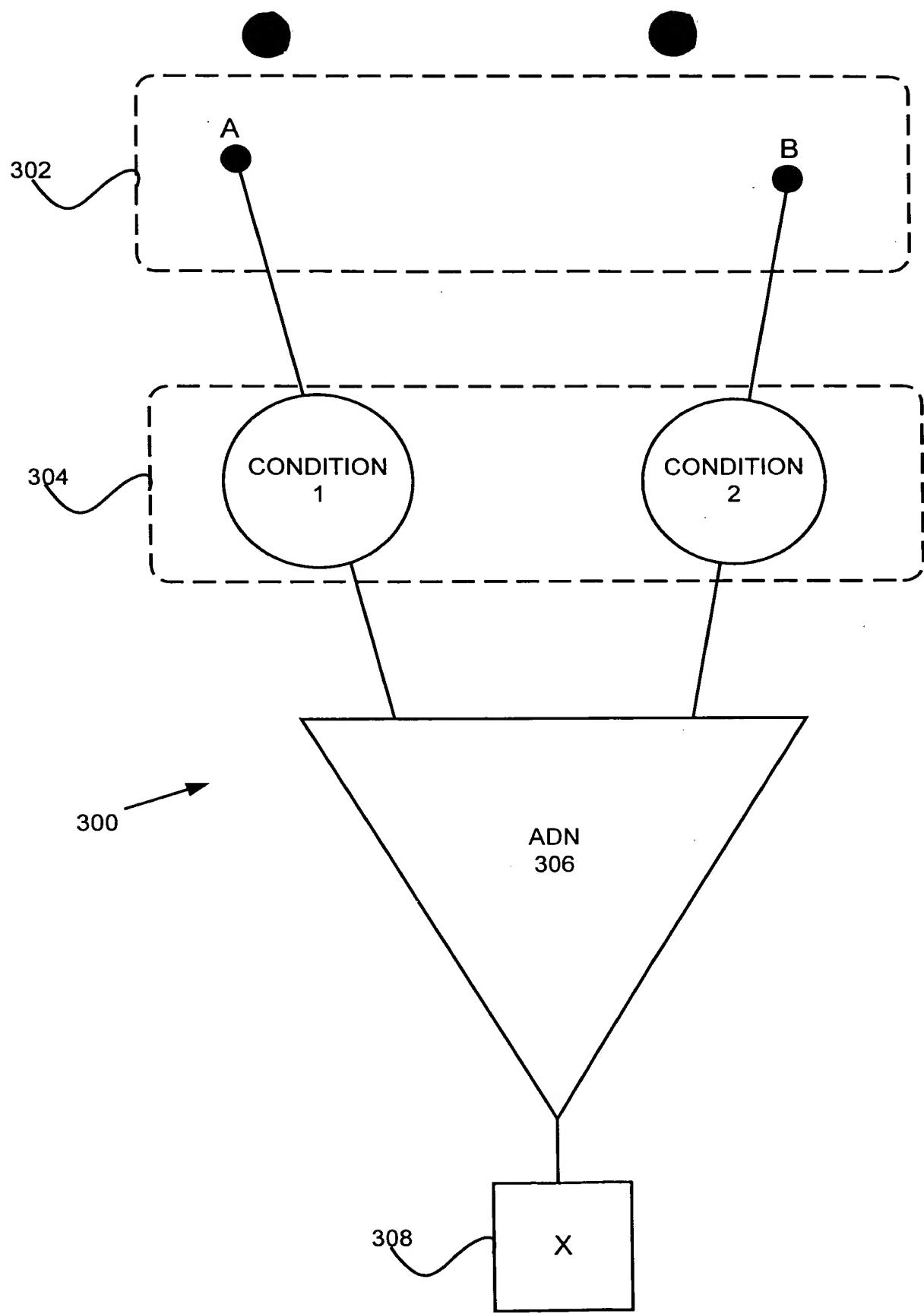


FIGURE 3A

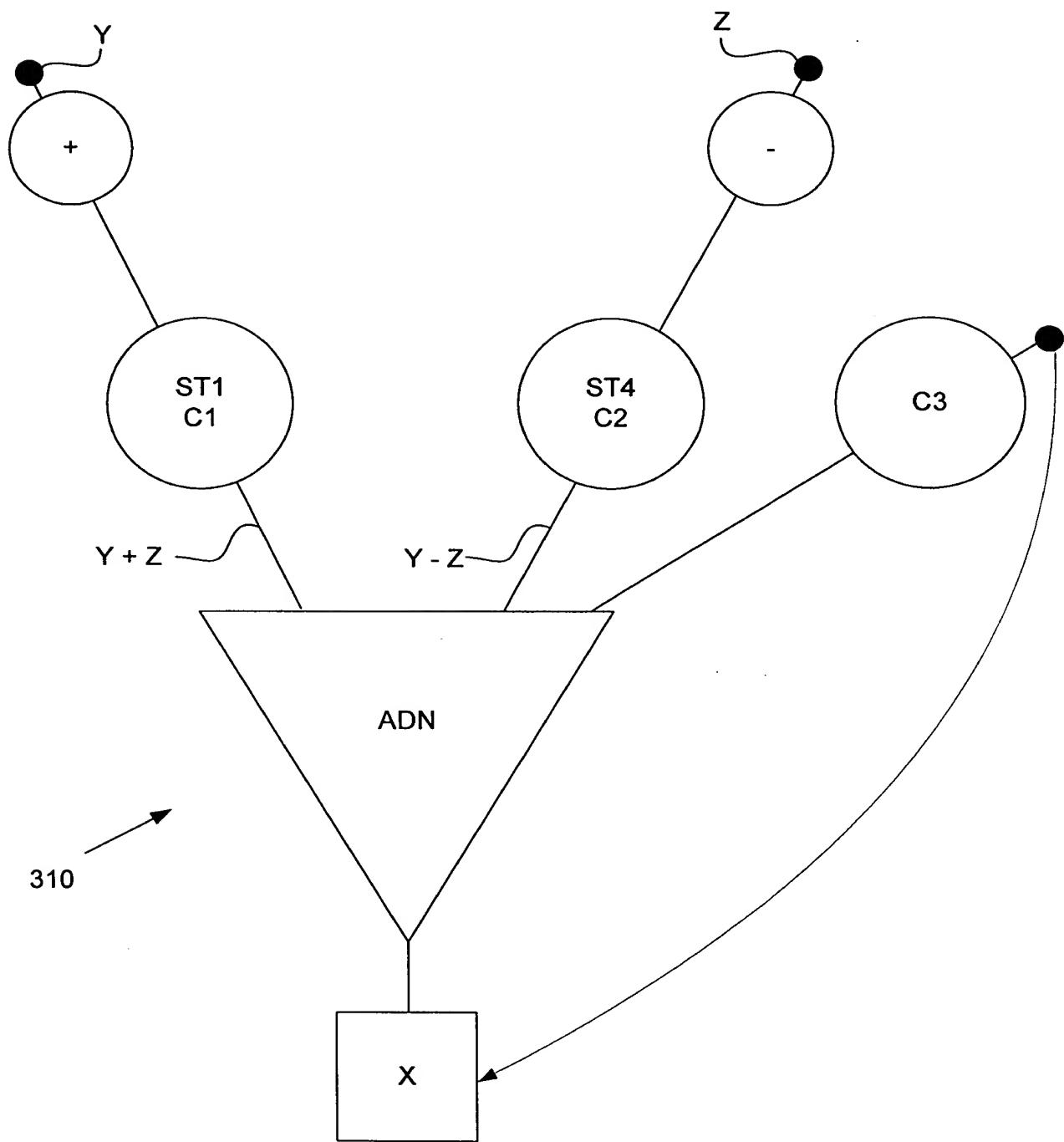


Figure 3B

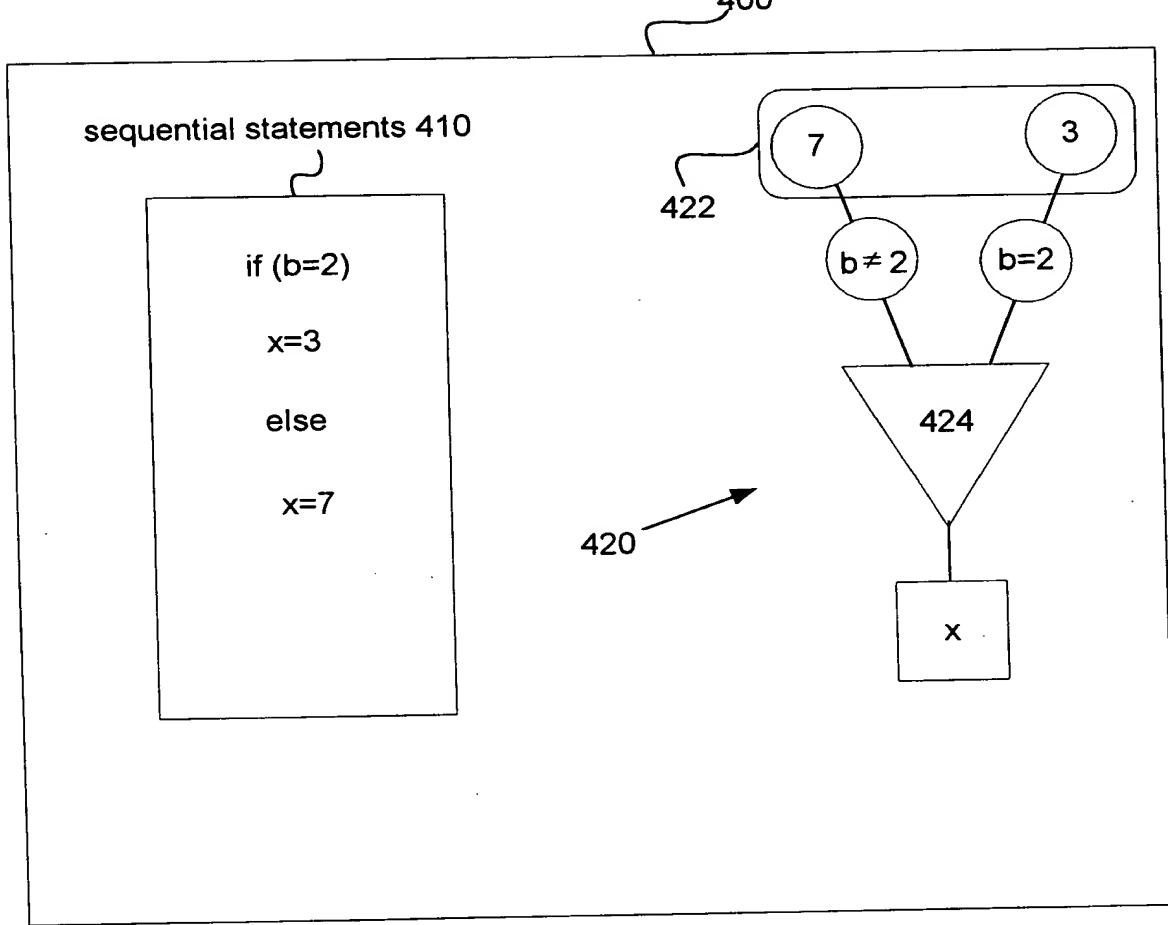


FIGURE 4A

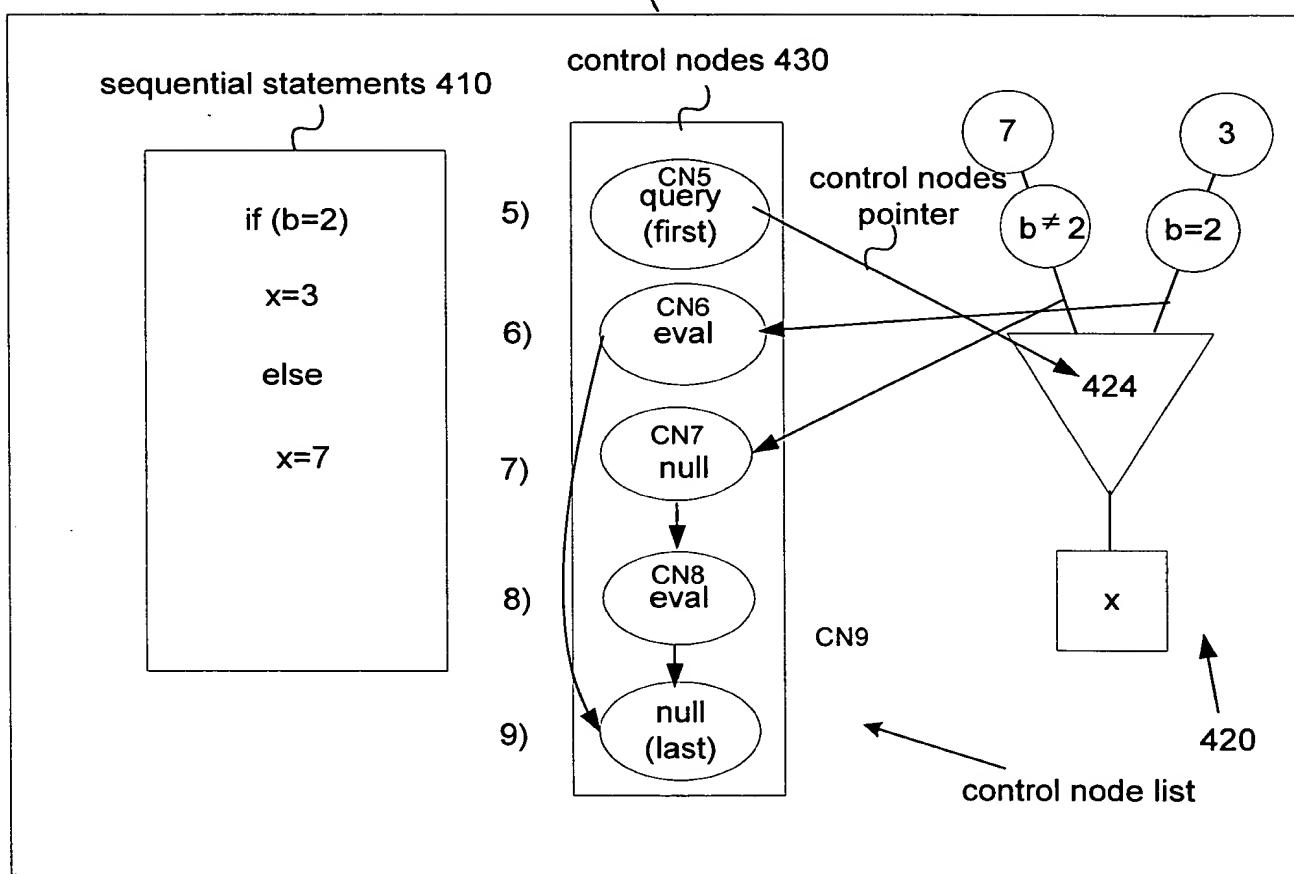


FIGURE 4B

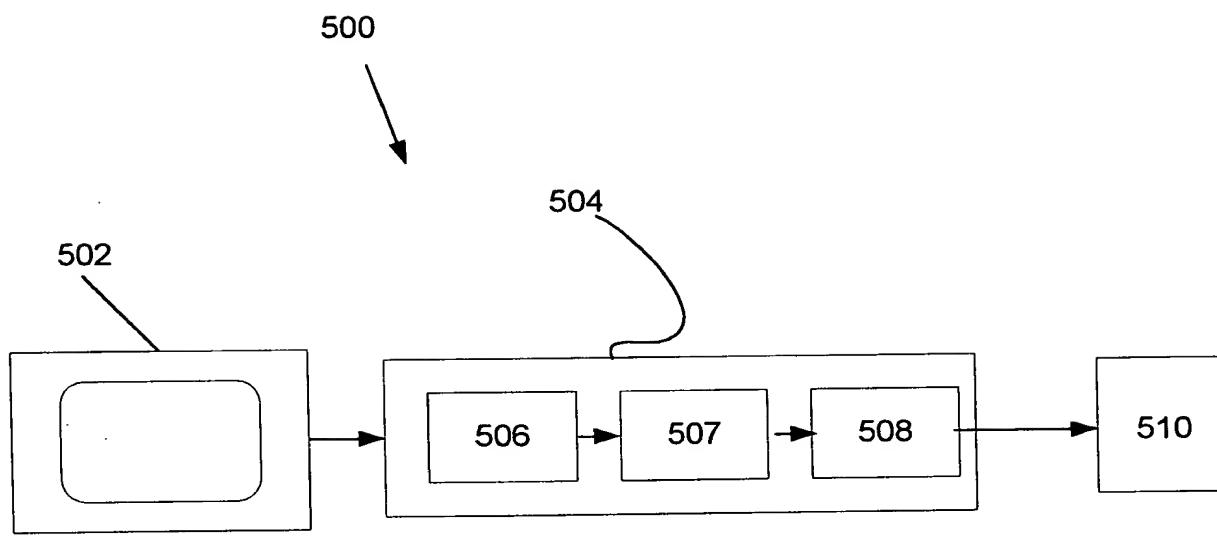


FIGURE 5A

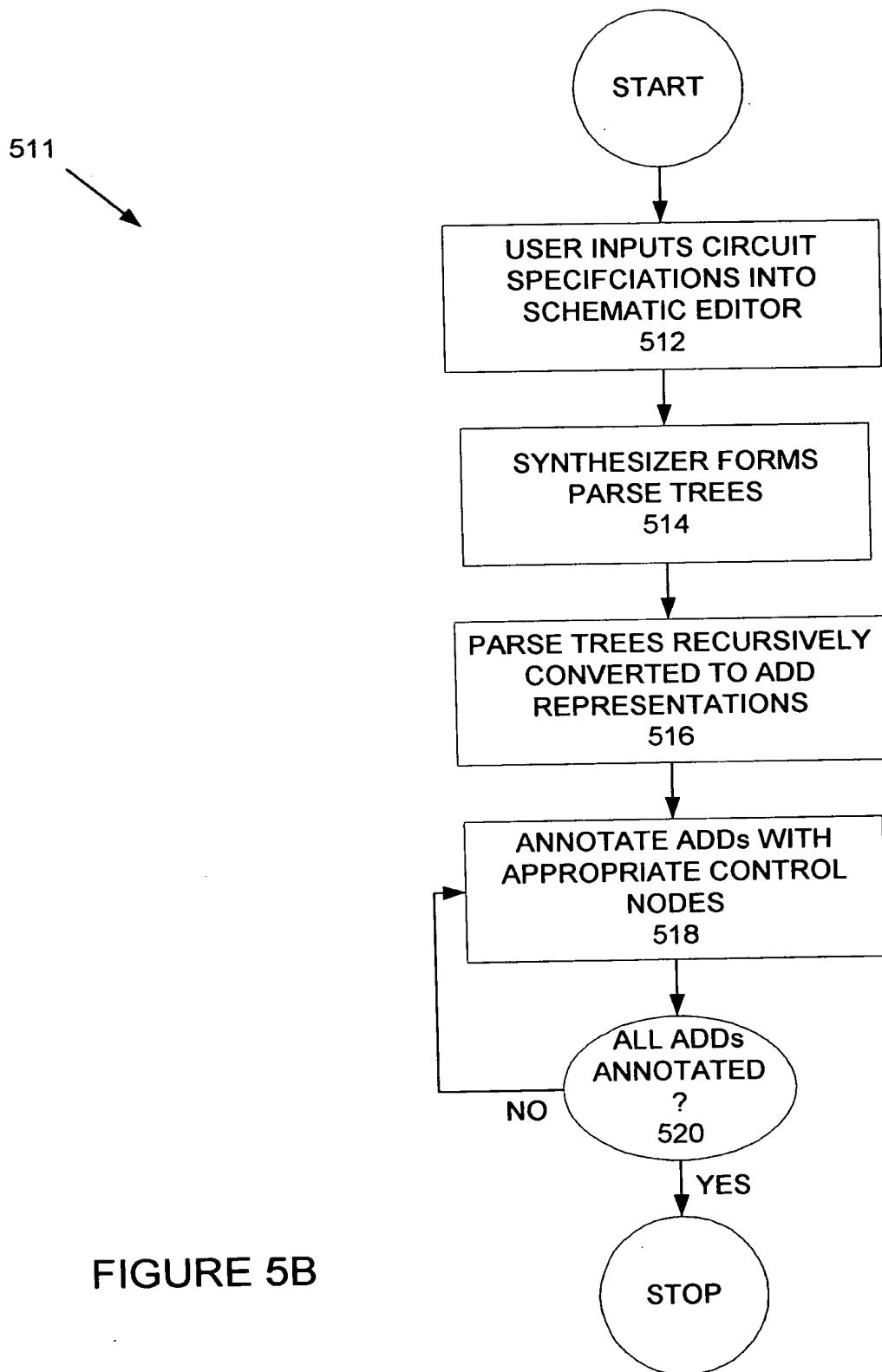


FIGURE 5B

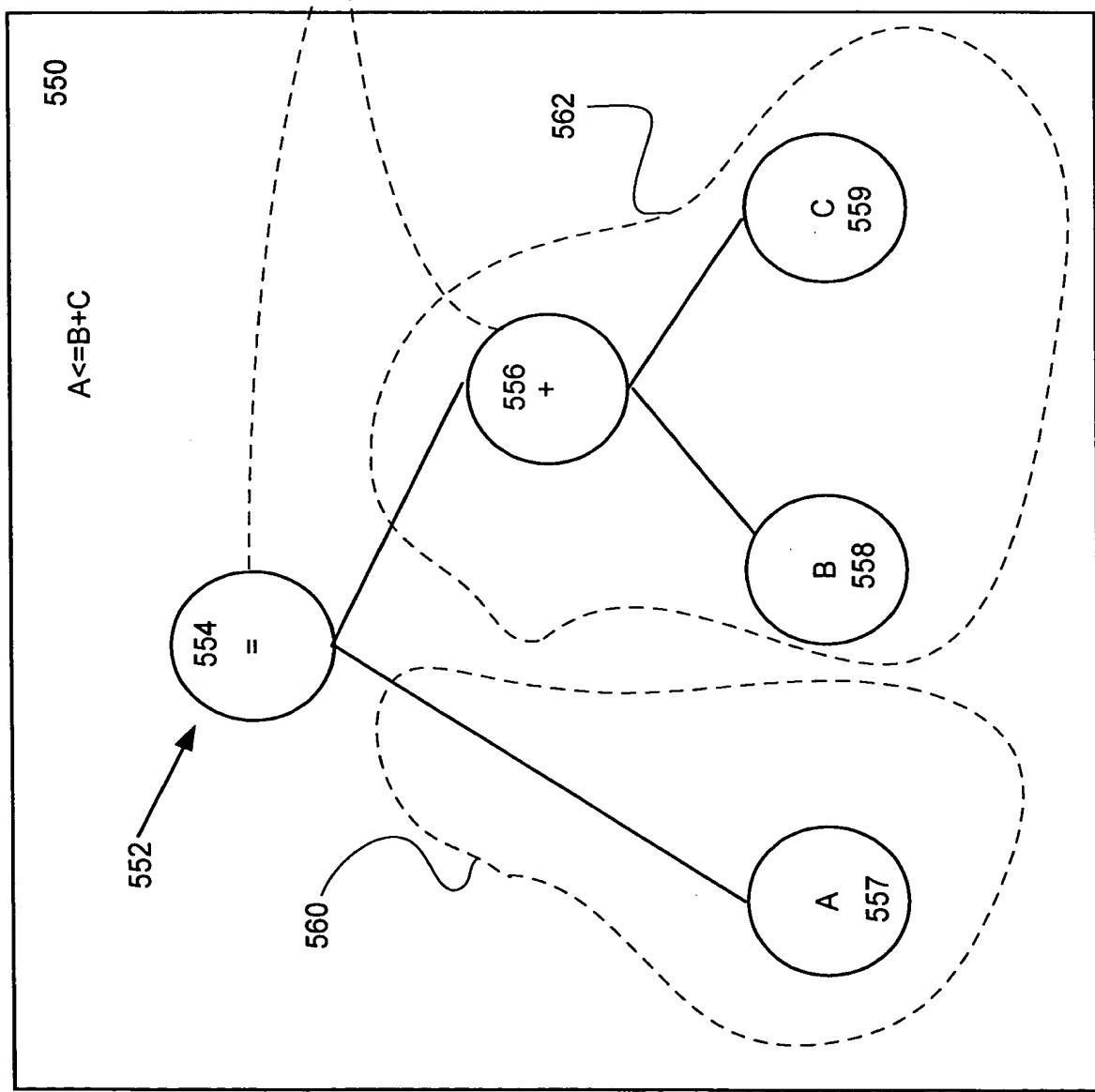


FIGURE 5C

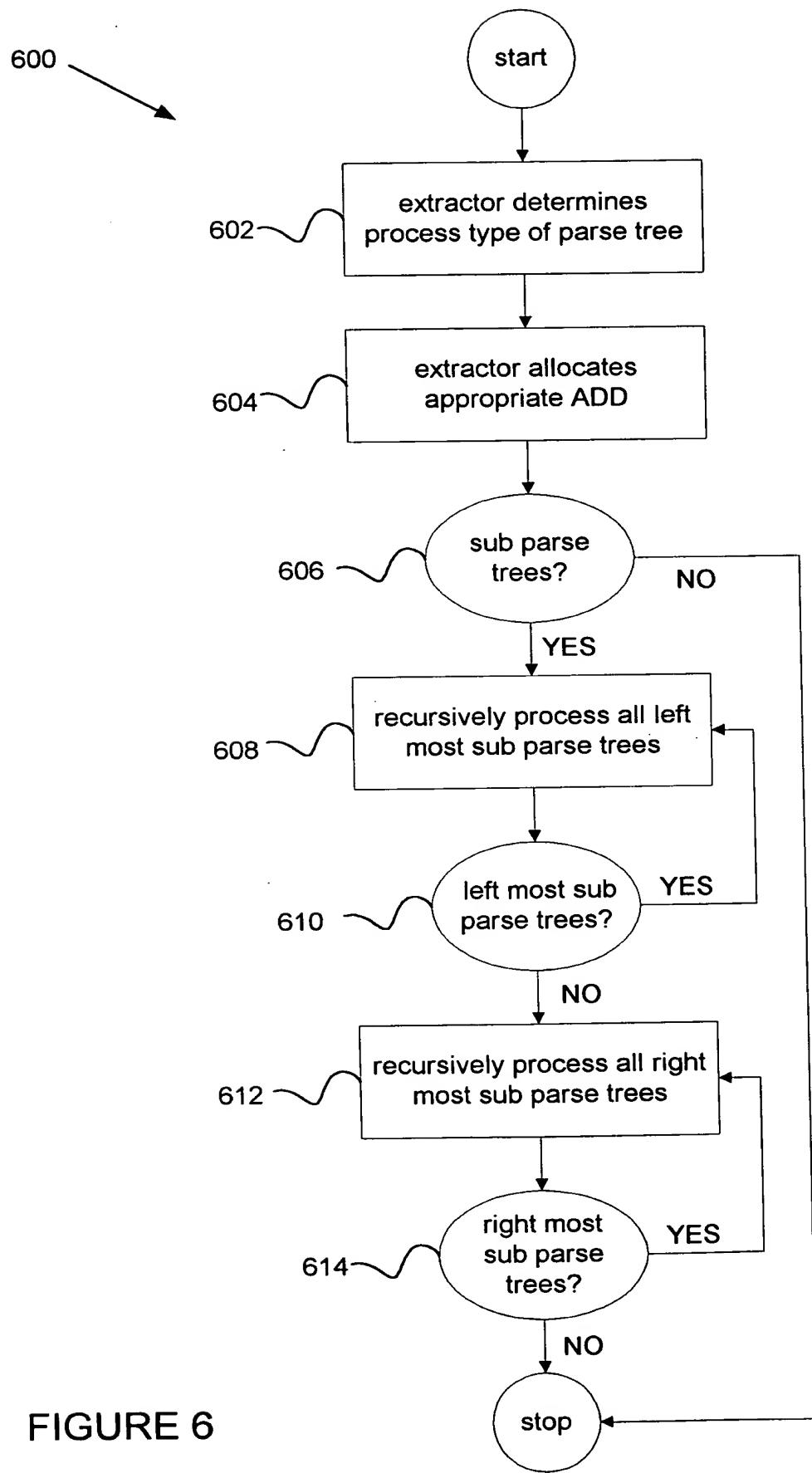
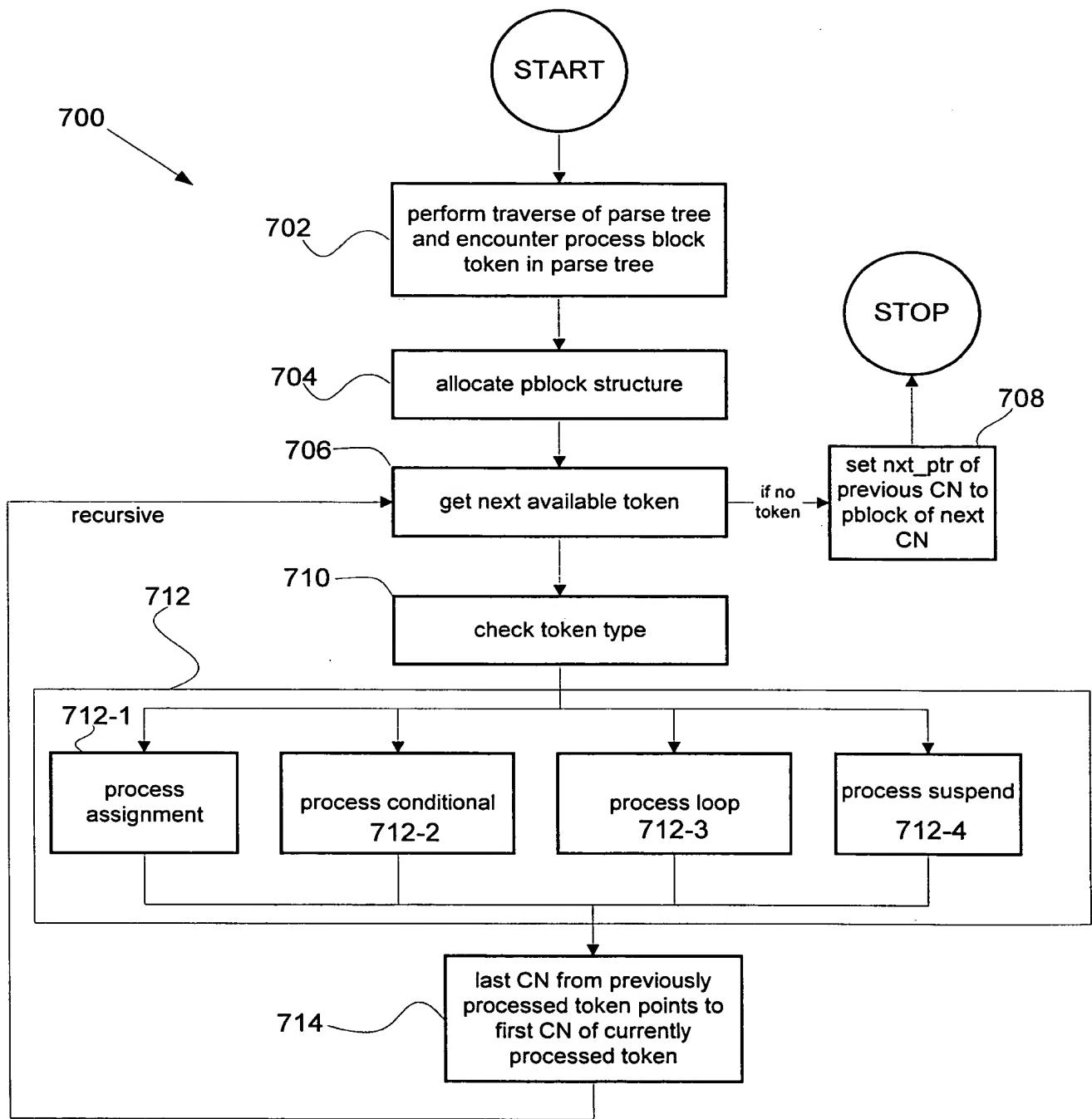


FIGURE 6



**FIGURE 7**

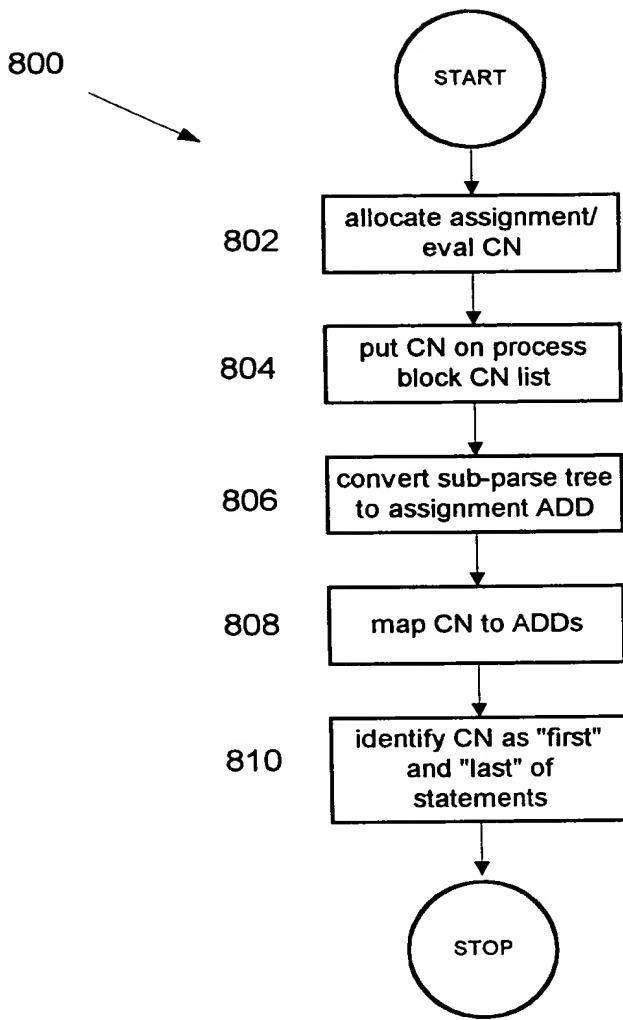


FIGURE 8A

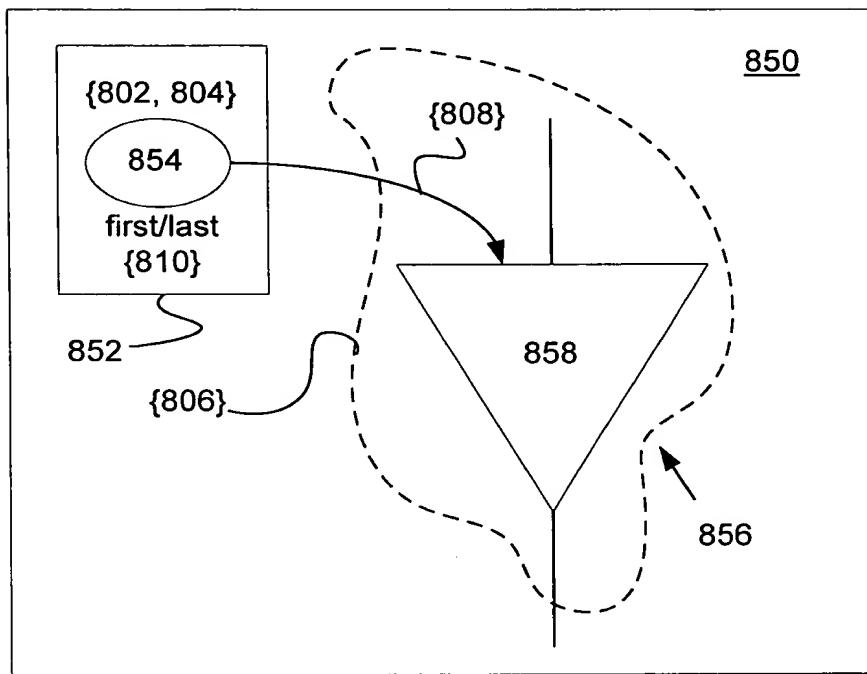


FIGURE 8B

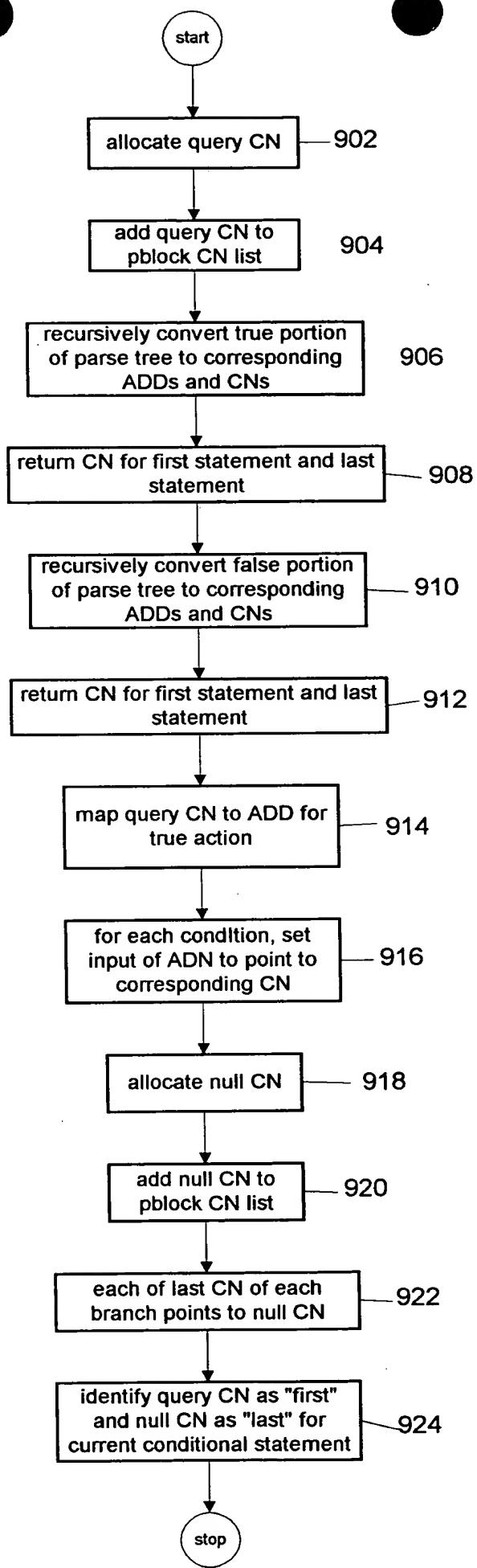
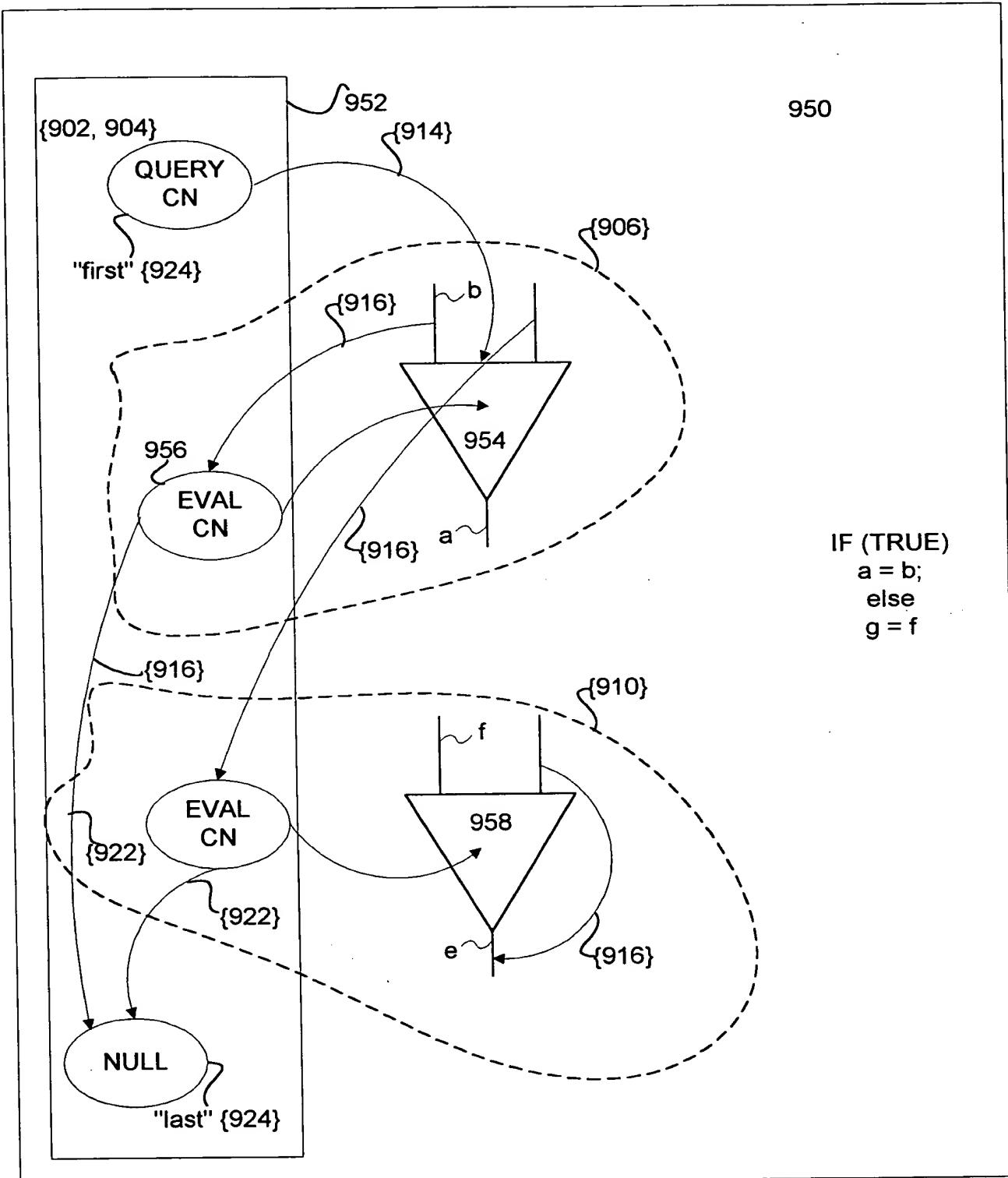
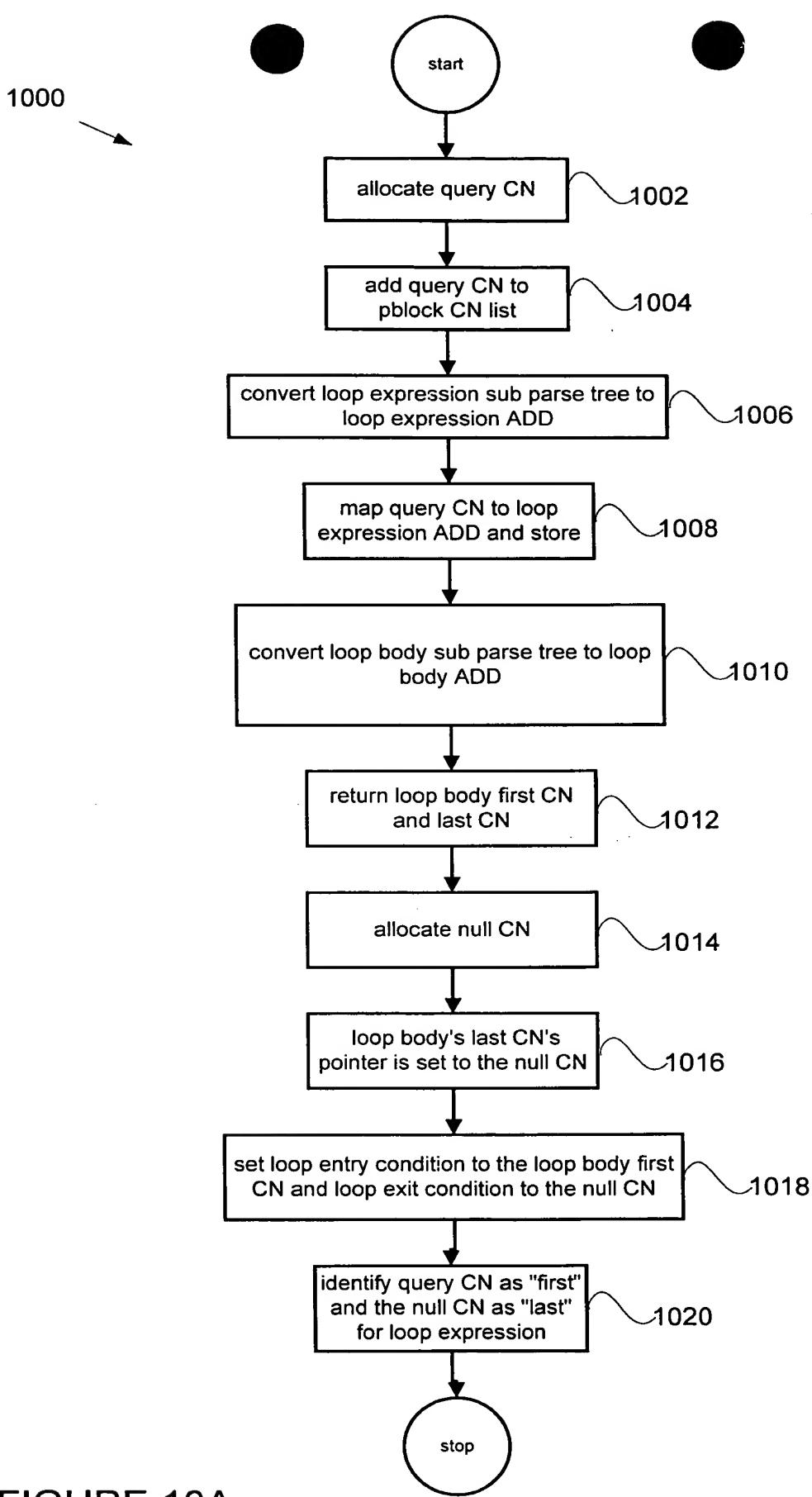


FIGURE 9A

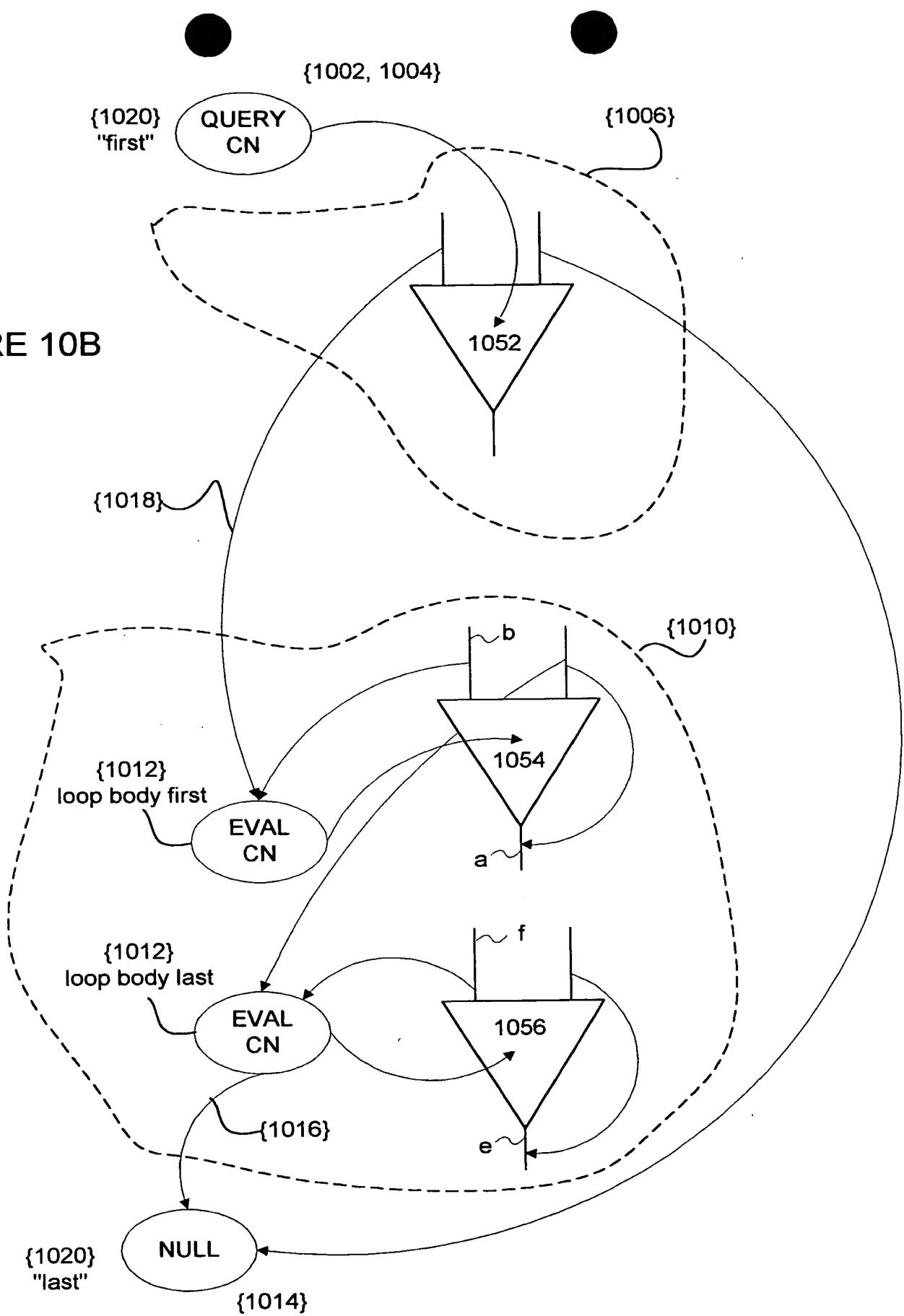


## FIGURE 9B



**FIGURE 10A**

FIGURE 10B



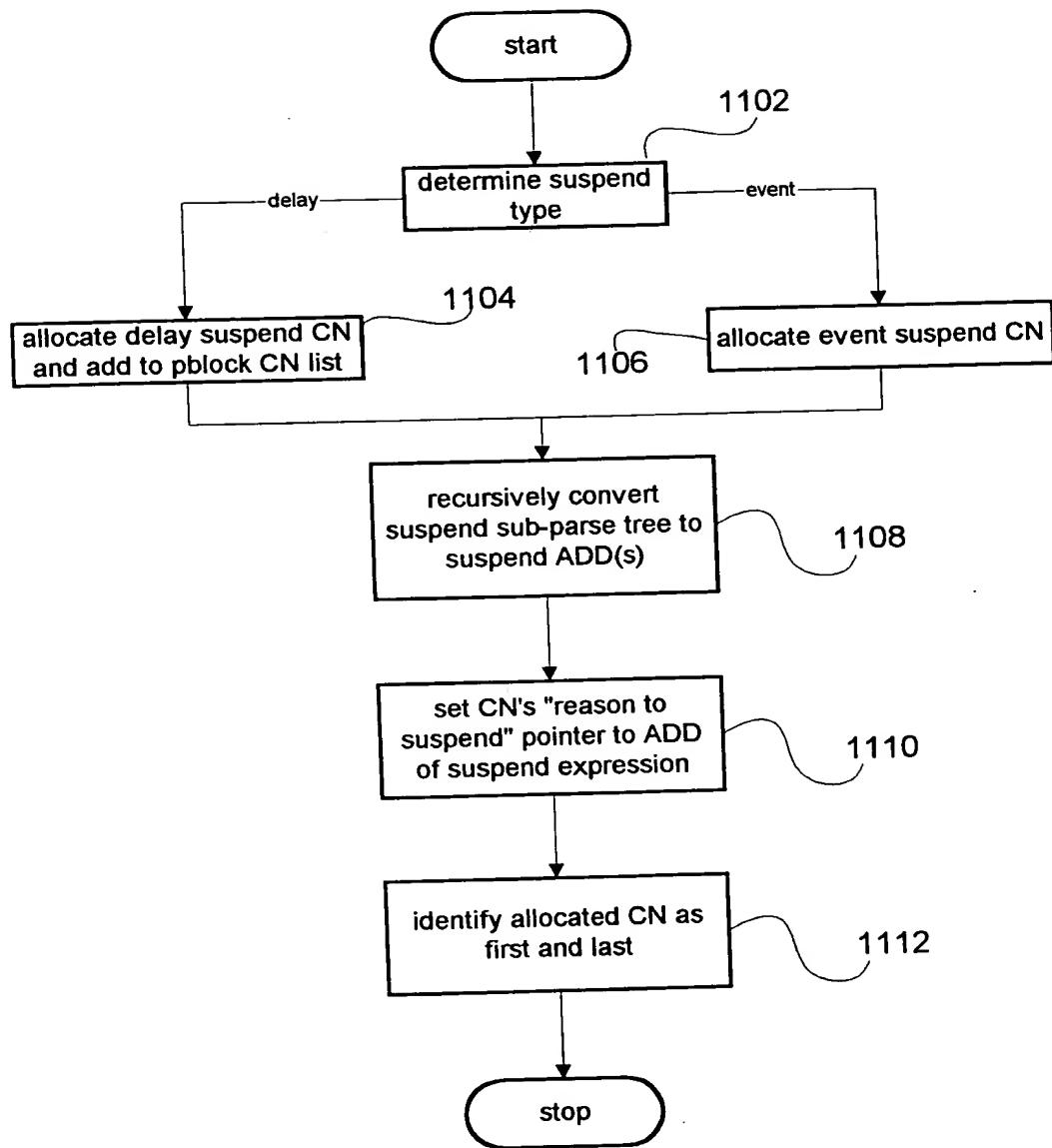


FIGURE 11A

FIGURE 11B

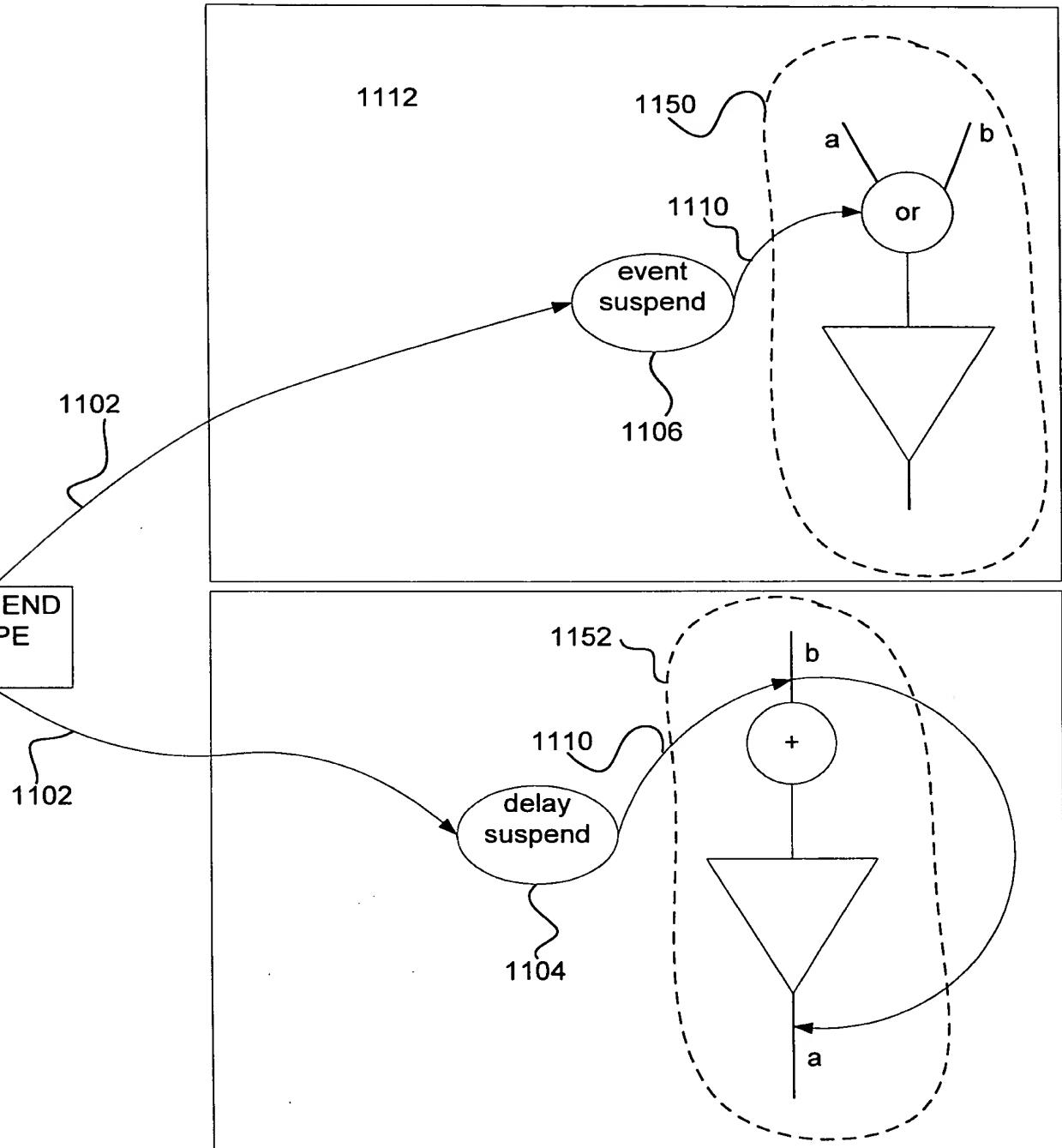


FIGURE 11B

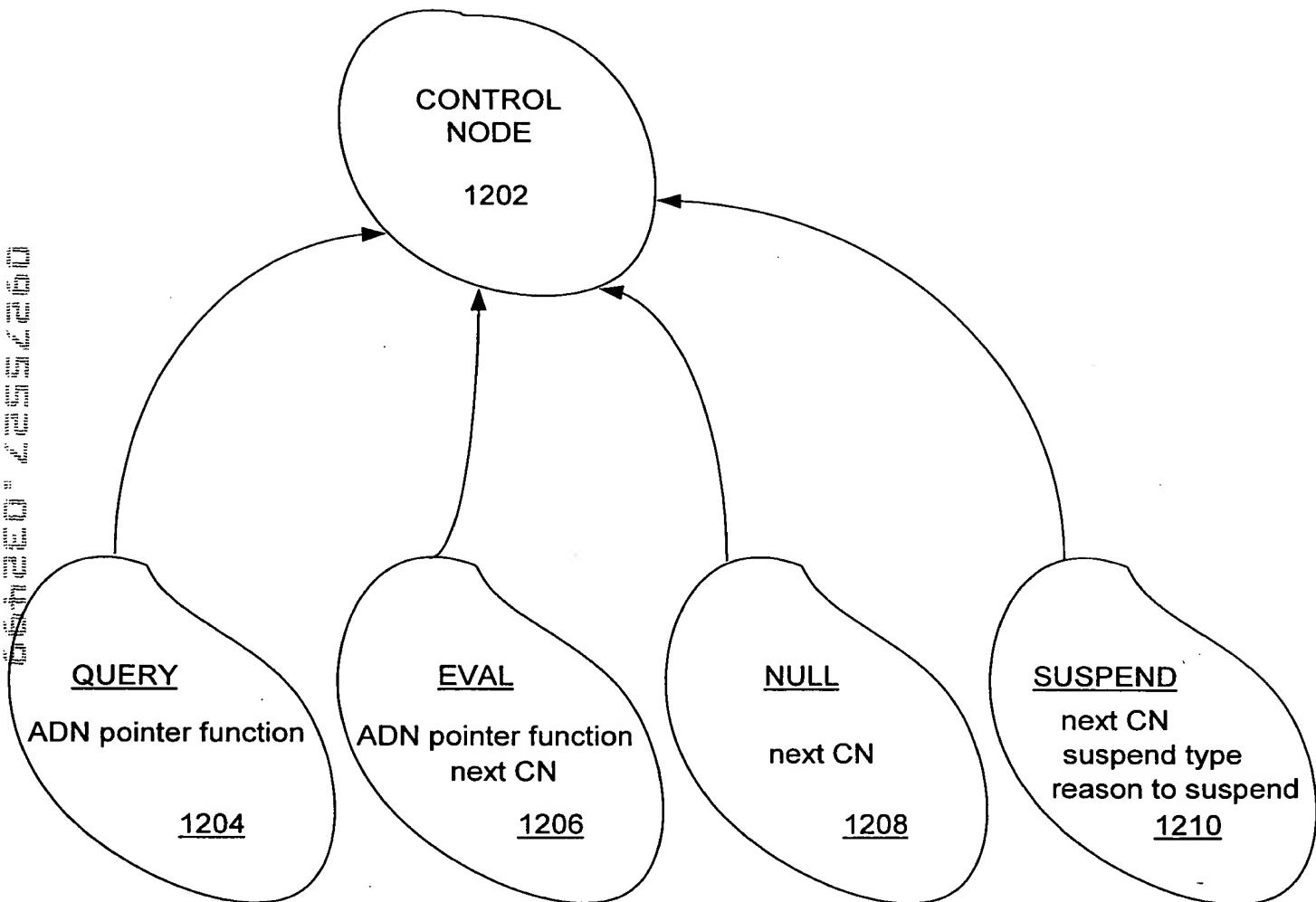
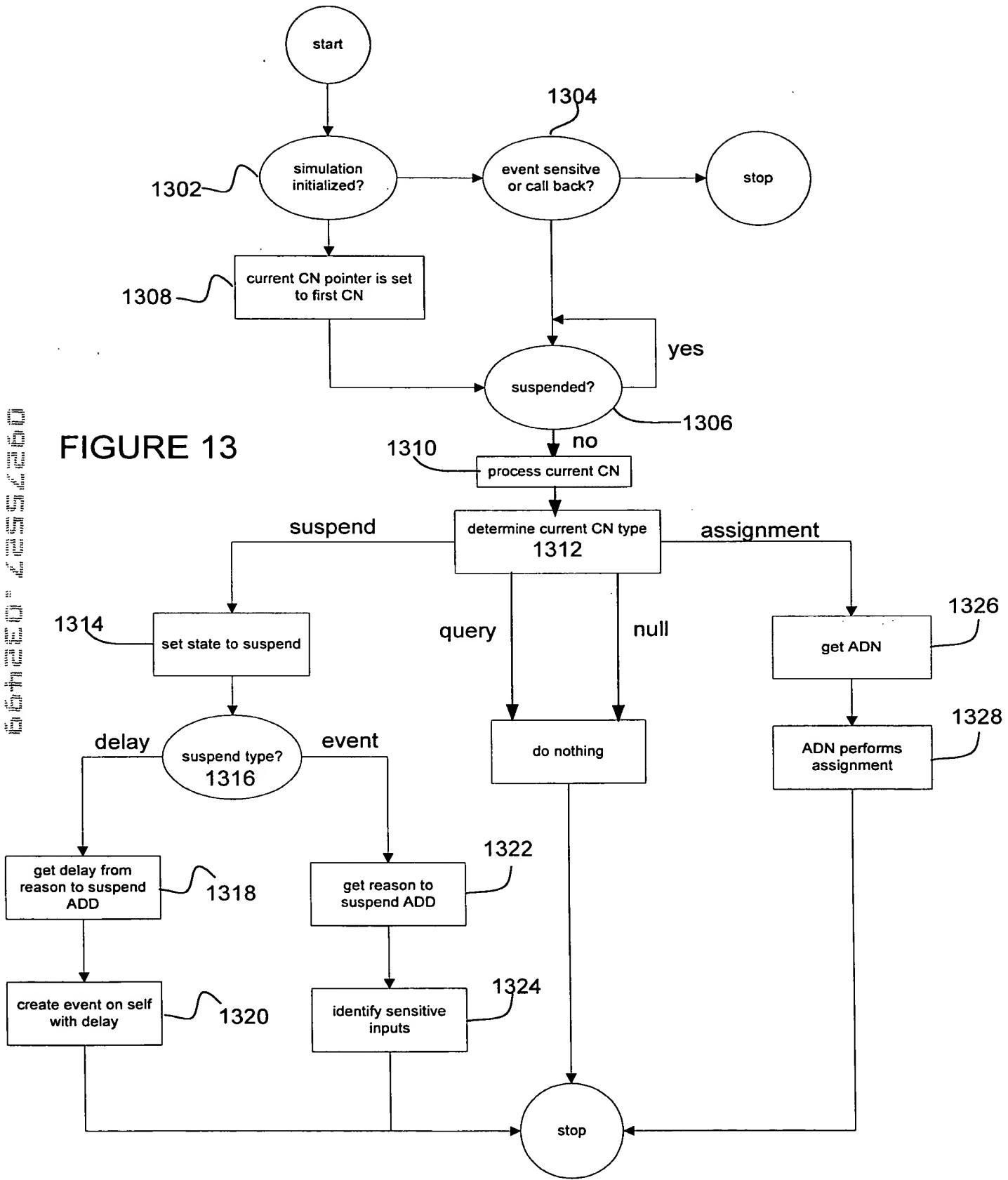
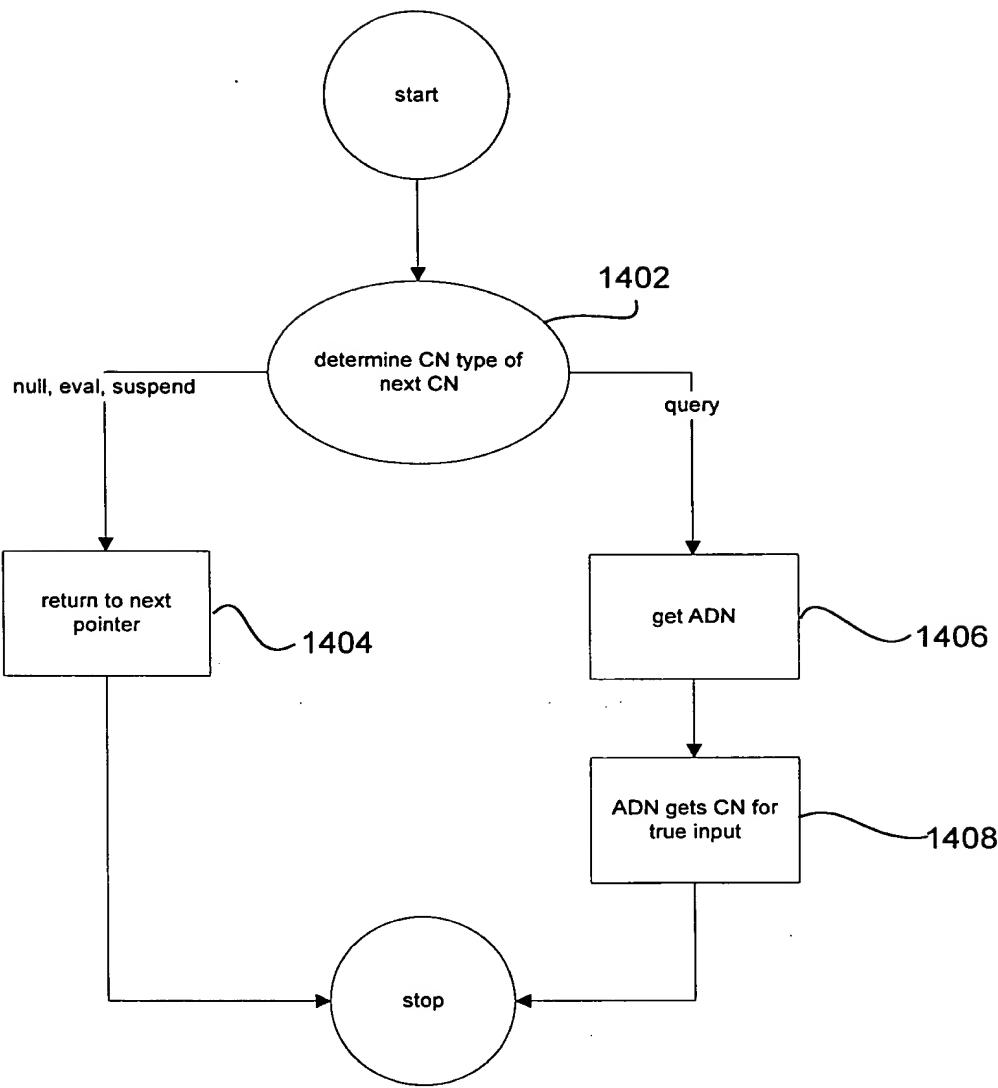


FIGURE 12

**FIGURE 13**



**FIGURE 14**

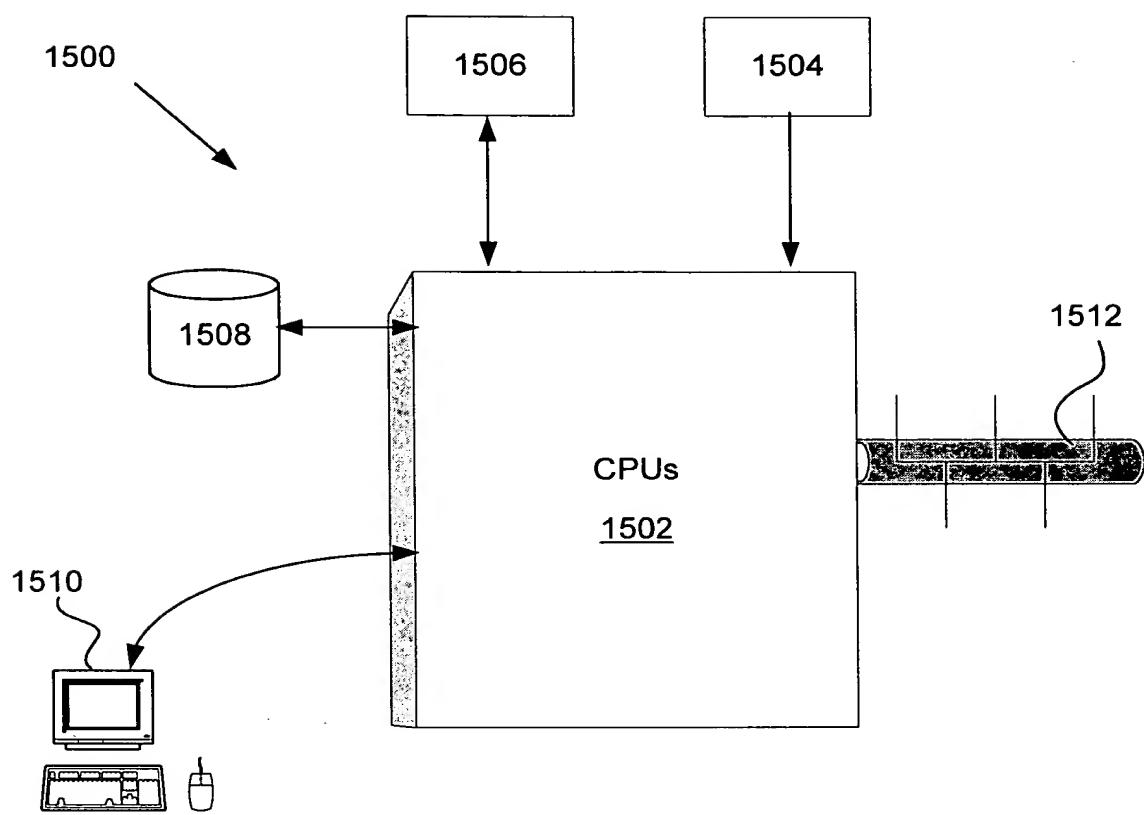


FIGURE 15

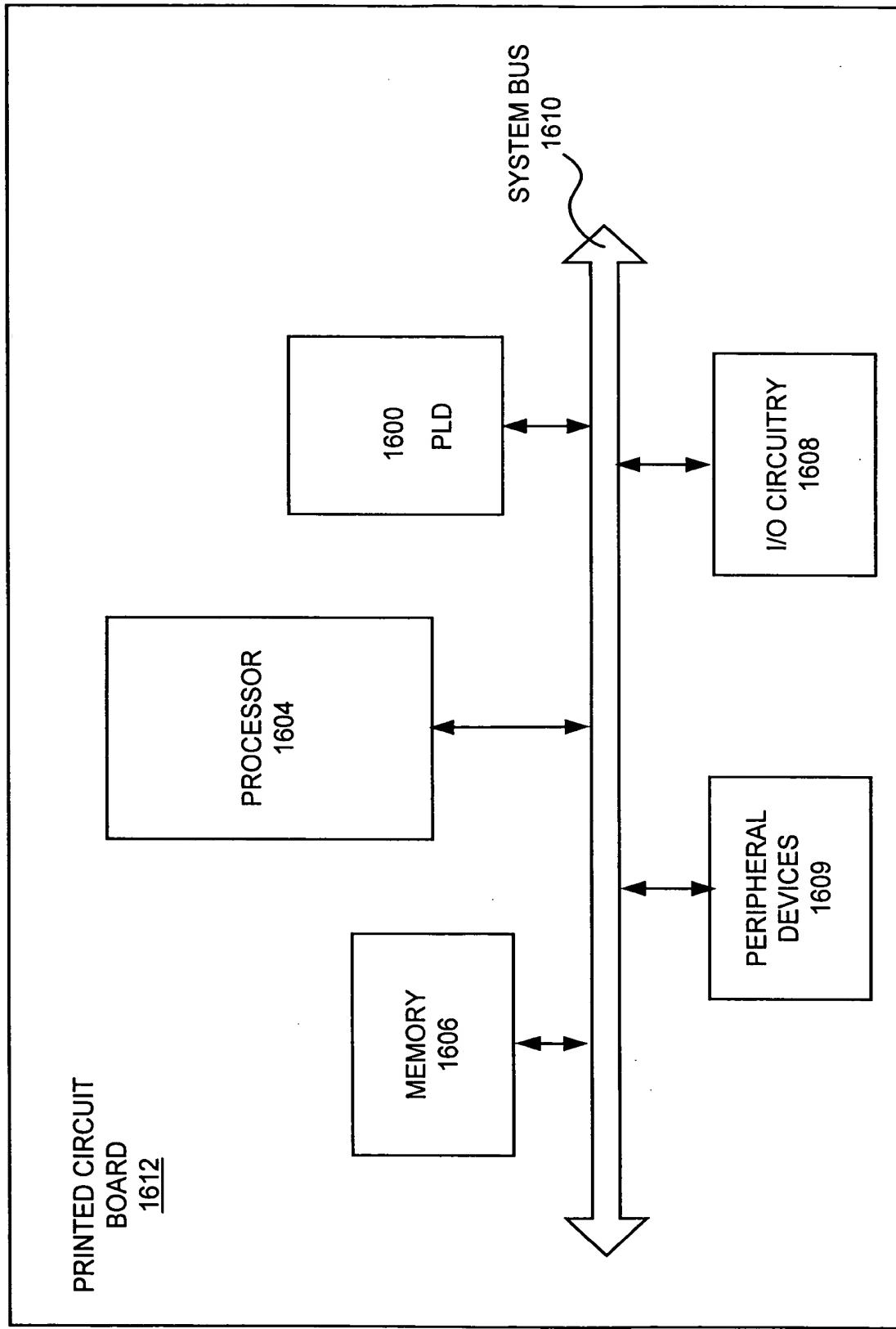


FIGURE 16